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☐ 9. Document ID: US 6170001 B1

L1: Entry 9 of 33

File: USPT

Jan 2, 2001

ces Altay inents Claims

US-PAT-NO: 6170001

DOCUMENT-IDENTIFIER: US 6170001 B1

TITLE: System for transfering format data from format register to memory wherein format data indicating the distribution of single or double precision data type in the register bank

Full Title Citation Front Review Classification Date Reference Securices Attachments Claims KMIC Draw Da □ 10. Document ID: US 6061711 A L1: Entry 10 of 33 File: USPT

US-PAT-NO: 6061711

DOCUMENT-IDENTIFIER: US 6061711 A

TITLE: Efficient context saving and restoring in a multi-tasking computing system

environment

Full Title Citation Front Review Classification Date Reference Section Stackments Claims KMC Draw, De . . . (

☐ 11. Document ID: US 6058465 A

L1: Entry 11 of 33

File: USPT

May 2, 2000

May 9, 2000

US-PAT-NO: 6058465

DOCUMENT-IDENTIFIER: US 6058465 A

TITLE: Single-instruction-multiple-data processing in a multimedia signal processor

Title Citation Front Review Classification Date Reference Sention Attachments Claims KMC Draw, De ☐ 12. Document ID: US 5898897 A L1: Entry 12 of 33 File: USPT Apr 27, 1999

US-PAT-NO: 5898897

DOCUMENT-IDENTIFIER: US 5898897 A

TITLE: Bit stream signal feature detection in a signal processing system

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KWIC Draw De

☐ 13. Document ID: US 5838984 A

L1: Entry 13 of 33

File: USPT

Nov 17, 1998

Oct 13, 1998

US-PAT-NO: 5838984

-DOCUMENT-IDENTIFIER: US 5838984 A

TITLE: Single-instruction-multiple-data processing using multiple banks of vector

registers

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

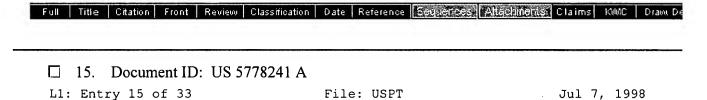
File: USPT

US-PAT-NO: 5821886

DOCUMENT-IDENTIFIER: US 5821886 A

L1: Entry 14 of 33

TITLE: Variable length code detection in a signal processing system



US-PAT-NO: 5778241

DOCUMENT-IDENTIFIER: US 5778241 A

TITLE: Space vector data path

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	K	MC Drav
	16.	Docum	ent ID	: US 5	745721 A							······
т.1 •	Ent	ry 16 c	of 33			File:	USPT			Apr 2	8.	1998

US-PAT-NO: 5745721

DOCUMENT-IDENTIFIER: US 5745721 A

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TITLE: Partitioned addressing apparatus for vector/scalar registers

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWAC	Drawu De
	17.	Docum	ent ID	: US 5	717881 A							

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L1: Entry 17 of 33

File: USPT

Feb 10, 1998

US-PAT-NO: 5717881

DOCUMENT-IDENTIFIER: US 5717881 A

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TITLE: Data processing system for processing one and two parcel instructions

Full Title Citation Front Review Classification Date Reference **Sequences Attachments** Claims KWC Draw De

☐ 18. Document ID: US 5706490 A

L1: Entry 18 of 33

File: USPT

Jan 6, 1998

US-PAT-NO: 5706490

DOCUMENT-IDENTIFIER: US 5706490 A

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TITLE: Method of processing conditional branch instructions in scalar/vector

processor

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KMC Draw De

☐ 19. Document ID: US 5659706 A

L1: Entry 19 of 33

File: USPT

Aug 19, 1997

US-PAT-NO: 5659706

DOCUMENT-IDENTIFIER: US 5659706 A

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TITLE: Vector/scalar processor with simultaneous processing and instruction cache

filling

Full Title Citation Front Review Classification Date Reference **Sequences Attachments** Claims KWIC Draw De

☐ 20. Document ID: US 5640524 A

L1: Entry 20 of 33

File: USPT

Jun 17, 1997

US-PAT-NO: 5640524

DOCUMENT-IDENTIFIER: US 5640524 A

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TITLE: Method and apparatus for chaining vector instructions

Full Title Citation Front Review Classification Date Reference Seguences Attachments Claims KWIC Draw De

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